

Electronic Acknowledgement Receipt

EFS ID:	1729064
Application Number:	10728492
International Application Number:	
Confirmation Number:	9752
Title of Invention:	Low power memory controller with leaded double data rate DRAM package arranged on a two layer printed circuit board
First Named Inventor/Applicant Name:	Eric Hung
Correspondence Address:	Leo Peters LSI Logic Corporation MS D-106 1621 Barber Lane Milpitas CA 95035 US - -
Filer:	Kevin L. Daffer/Pamela Gerik
Filer Authorized By:	Kevin L. Daffer
Attorney Docket Number:	03-1237 (5201-27600)
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Application Type:	Utility

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1		ROA_1-29-07.pdf	127040	yes	14

Multipart Description/PDF files in .zip description				
Document Description		Start	End	
Amendment - After Non-Final Rejection		1	1	
Specification		2	3	
Claims		4	8	
Applicant Arguments/Remarks Made in an Amendment		9	14	

Warnings:

Information:

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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